

FIG. 1

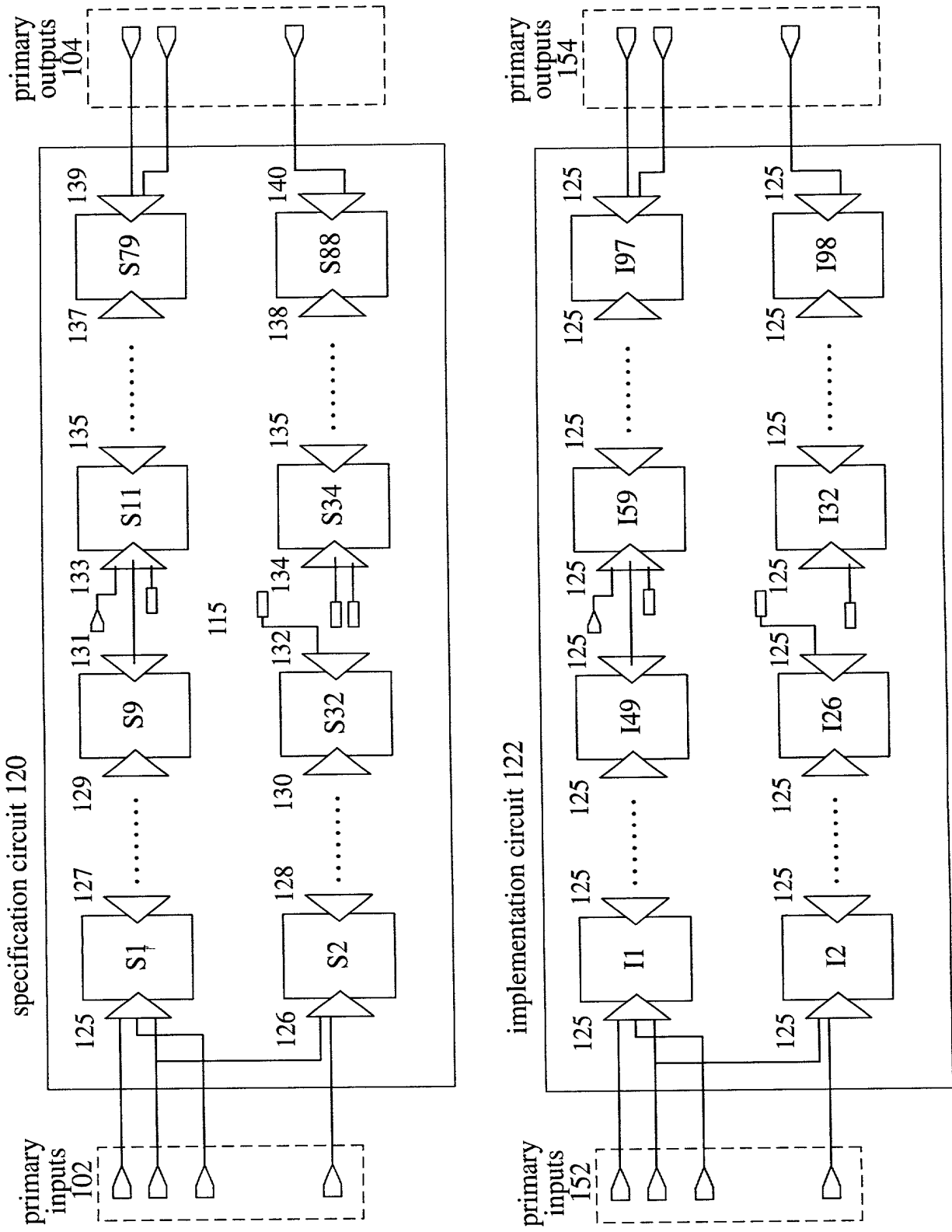
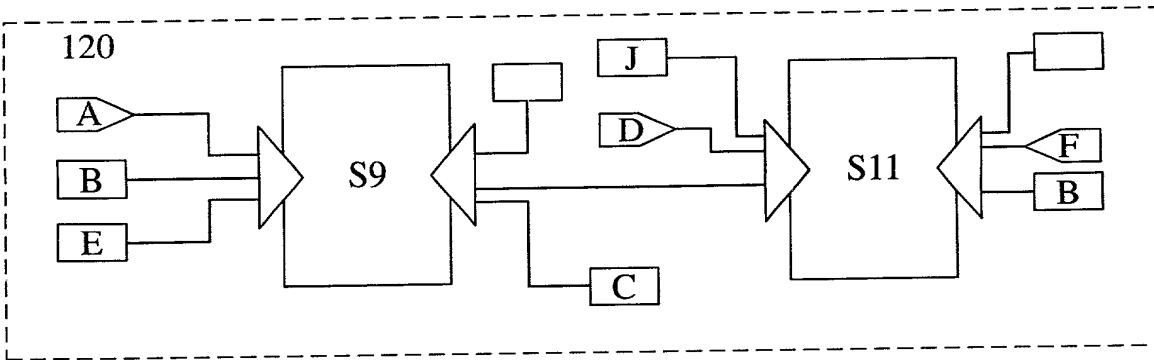


FIG. 2



STAGE 1

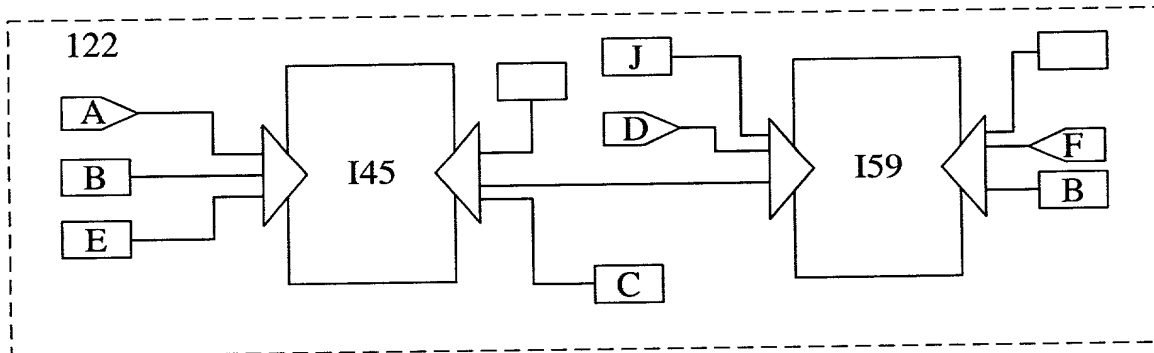
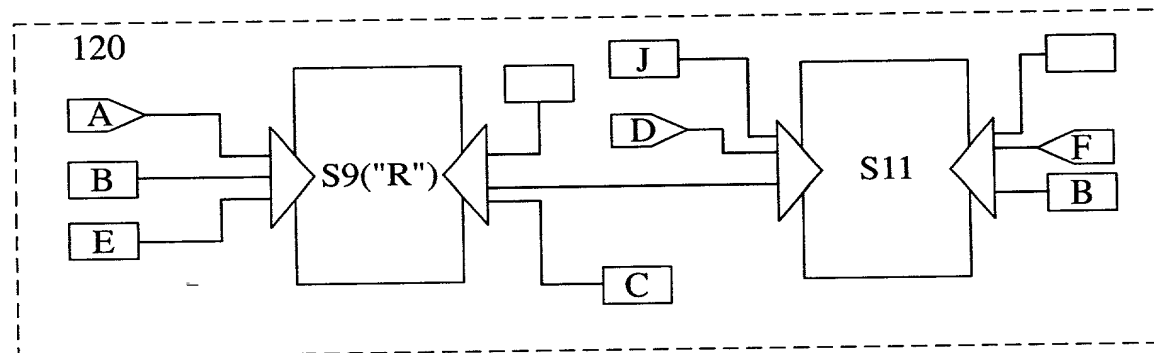


FIG. 3a



STAGE 2

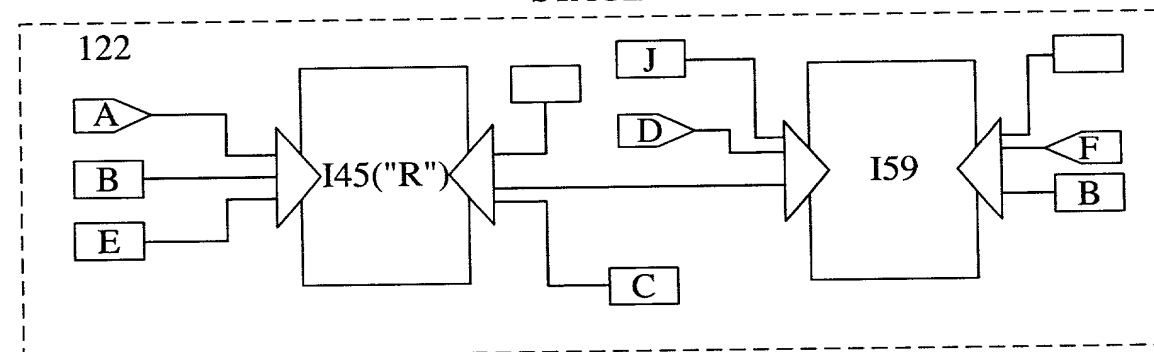
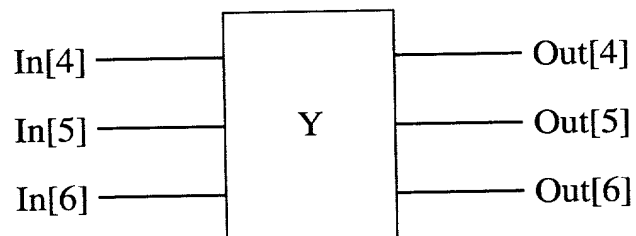
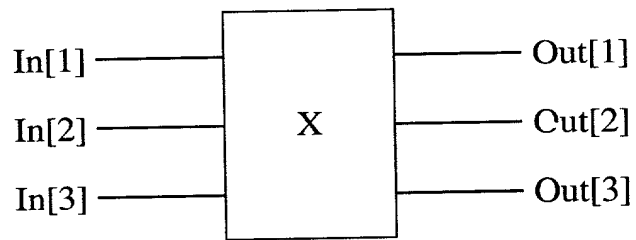


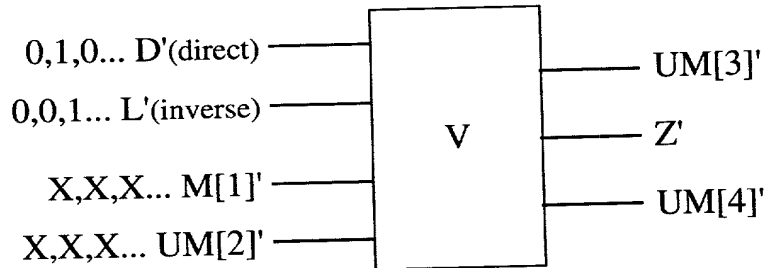
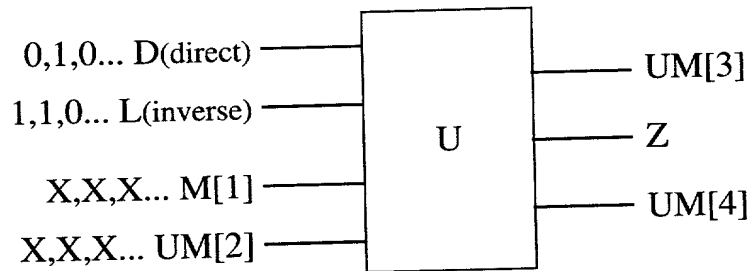
FIG. 3b





250	In[1]	In[2]	In[3]	Out[1]	Out[2]	Out[3]
	0	0	0	0	1	0
	0	0	1	1	1	1
	0	1	0	0	1	1
Don't care input	0	1	1	1	0	1
	1	0	0	0	0	0
Don't care input	1	0	1	1	0	1
	1	1	0	1	1	0
	1	1	1	0	1	0
255	In[4]	In[5]	In[6]	Out[4]	Out[5]	Out[6]
	0	0	0	0	1	0
	0	0	1	1	1	1
	0	1	0	0	1	1
Don't care input	0	1	1	0	1	1
	1	0	0	0	0	0
Don't care input	1	0	1	0	0	1
	1	1	0	1	1	0
	1	1	1	0	1	0

FIG. 5



270

	D	L	D'	L'	M[1]	M[1]'	UM[1]	UM[1]'	Z	Z'
1	0	1	0	0	X	X	X	X	0	1
2	1	1	1	0	X	X	X	X	1	0
3	0	0	0	1	X	X	X	X	0	1
.										
.										
.										
30	1	0	1	1	X	X	X	X	0	1
31	0	1	0	0	X	X	X	X	0	1
32	1	0	1	1	X	X	X	X	X	X

FIG. 6

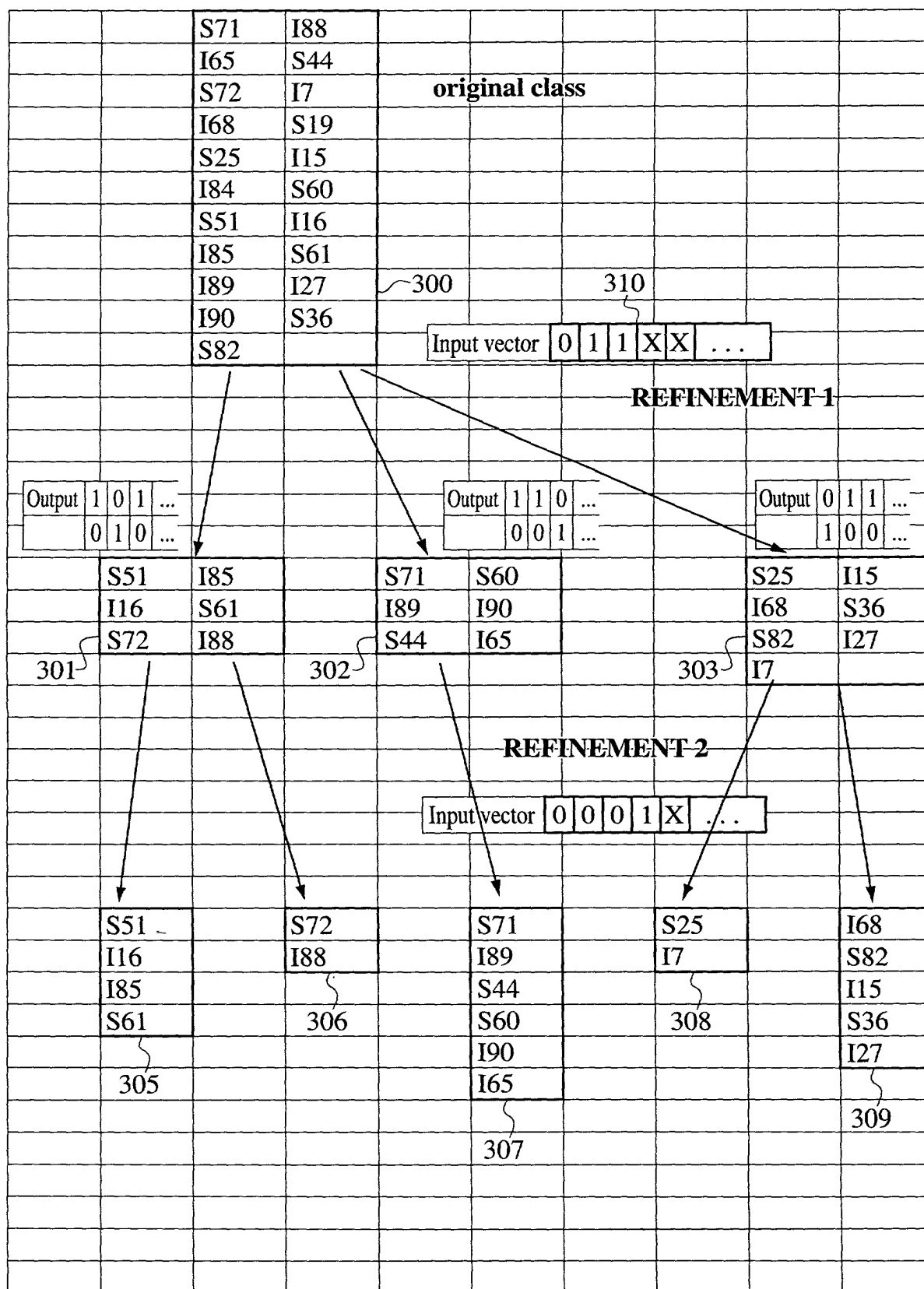


FIG. 7

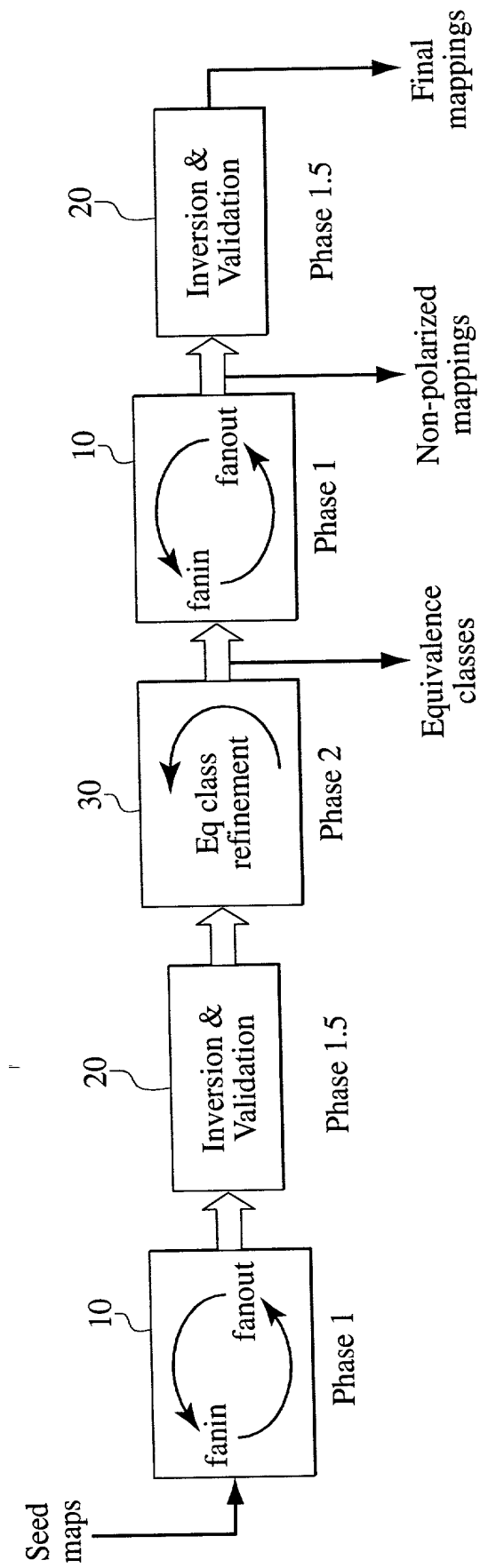


FIG. 8